

STUDY OF DYNAMIC VOLTAGE RESTORER (DVR) FOR DIMINUTION OF VOLTAGE SAGS

Jashmin Kiran¹, Prof. Lalit Jain²

¹M.tech Scholar, ²Head of Department, Oriental College of Technology

Abstract: *Changing electric load and higher power move in a wide interconnected system lead stoserious, security need in control framework activities additionally if a sudden blame happens there is an adjustment in voltage profile which can prompt a sudden harm on stack end. These voltage lists are remunerated at generator side by different strategies however at the heap end there is a possibility. to stay away from such droop at stack side a tangle lab demonstrate is proposed in which a transmission line is bolstered with two sources out of which one is a breeze source and after that is subjected to a 3 stage blame. the voltage droop which happens is repaid in the other model at same moment when DVR Dynamic voltage restorer is customized and is associated at the midpoint of framework. The outcomes acquired demonstrates that the voltage is repaid and profile is adjusted, DVR utilizes the vitality source in all around planned way and infuses necessary AC voltage to framework.*

I. INTRODUCTION

The economy invested in the distribution system is large enough to take into account the concept of equipment protection against various disturbances that affects the reliability of not only the distribution system but the entire power system incorporating generation & transmission too. The wide acceptance of sophisticated electronic devices at the utility end deteriorates the quality of supply & utility is suffering from its bad effects on large scale. The various power quality problems[1] encompass the voltage sags, voltage dips & voltage swells, flickers, harmonics & transients accompanied by unbalanced power, which are results of various faults with three phase fault being the most severe among all, starting of induction motor which is most often used due to its rugged construction, switching off large loads and energizing of capacitor banks.

Voltage sag is one of most important power quality issues because the increasing usage of voltage sensitivity devices has made industrial processes more susceptible to supply voltage. Custom power devices are mainly used in voltage sag mitigation, protection and control of sensitive loads, reactive power and voltage regulation and harmonic elimination applications. There are different methods which have been proposed to mitigate the voltage sags like Uninterruptible Power Supplies (UPS), network reconfiguration devices like Static Transfer Switches (STS), DSTATCOM and series compensating devices like Dynamic Voltage Restorers (DVR).

The capability of DVR control schemes is demonstrated using MATLAB/SIMULINK simulations. The Simulink models have been developed for the distribution networks

with linear and non-linear loads. The proposed DVR for 10kV distribution line has been assumed to be located in medium voltage distribution network level and it can mitigate three-phase sags The DVR has been designed with special importance at the control of PWM inverter i.e. fuzzy logic control. On the distribution side there is always a probability of highly sensitive load like hospitals ,communities etc and therefore on switching multiple loads the voltage profile experiences a jerk sag which can damage the load hence voltage profile needs to be maintained which is dine through fuzzy logic controlled DVR

II. LITERATURE SURVEY DONE

C. Sankaran [1] introduced the clear description of power quality & its associated problems in power system. He presented the examples & steps to solve power quality problems in terms of illustrations, figures & their worst effects on power system performance leading to disruptions & substantial economic losses.

N.G.Hingorani et al. [2] introduced a technology popularly known as FACTS (flexible Ac transmission system) based on power electronics to enhance the controllability, stability & power transfer capability of ac transmission system. He revolutionized the area of power electronics by discussing in-depth the FACTS controllers

N.H. Woodley et al. The proposed DVR was installed on 12.47-kV system at an automated yarn manufacturing and weaving factory where it protected the plant from disturbances from the distribution system.

John Godsk Nielsen et al. [16] tested and controlled DVR with advanced technique at medium voltage level of 10kV.The DVR is tested for different methods to initiate voltage dips.

U. Vidhu Krishnan et al. [17] presented a control system based on dqo technique which is a scaled error between source side of the DVR and its reference for sags/swell correction. His work confirmed the effectiveness of the device in compensating voltage sags and swells with very fast response (relative to voltage sag/swell time) by MATLAB using simulation.

III. POWER QUALITY PROBLEMS

Power system transients- They are fast, short-duration events that produce distortions such as notching, ringing, and impulse. The mechanisms by which transient energy is propagated in power lines, transferred to other electrical circuits, and eventually dissipated are different from the factors that affect power frequency disturbances.

Voltage sag: It is a short duration disturbance. During voltage sag, r. m. s. voltage falls to a very low level for short

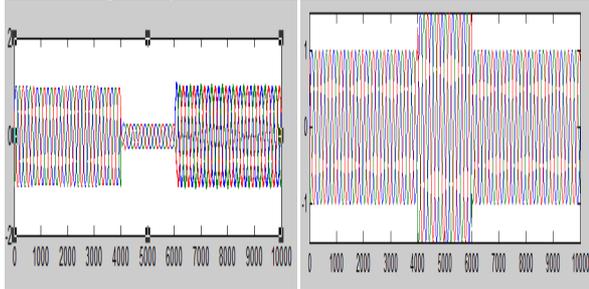
period of time.

Voltage swell: It is a short duration disturbance. During voltage sag, r. m. s. voltage increases to a very high level for short period of time.

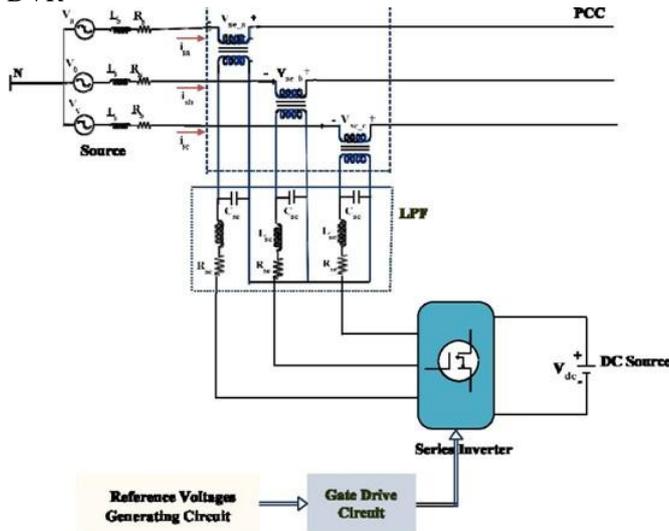
Flicker: It is visual effect and undesirable frequency variation of voltage in a system.

Ringing waves: Oscillatory disturbances of decaying magnitude for short period of time is known as ringing wave. It may be called a special type transient.

Outage: It is special type of interruption where power cut has occurred for not more than 60 s due to fault or mal-tripping of switchgear/system.



**POWERQUALITY SOLUTION
 DVR**



The first DVR was installed in North America in 1996 - a 12.47 kV system located in Anderson, South Carolina [13]. Practically, the capability of injection voltage by DVR system is 50% of nominal voltage. This allows DVRs to successfully provide protection against sags to 50% for durations of up to 0.1 seconds. Furthermore, most voltage sags rarely reach less than 50%. The dynamic voltage restorer is also used to mitigate the damaging effects of voltage swells, voltage unbalance and other waveform distortions. DVRs of capacities up to 50 MVA have seen applications to critical loads in food processing, semiconductor and utility supply. Cost and installation constraints limit these to where there is clear need for constant voltage supply

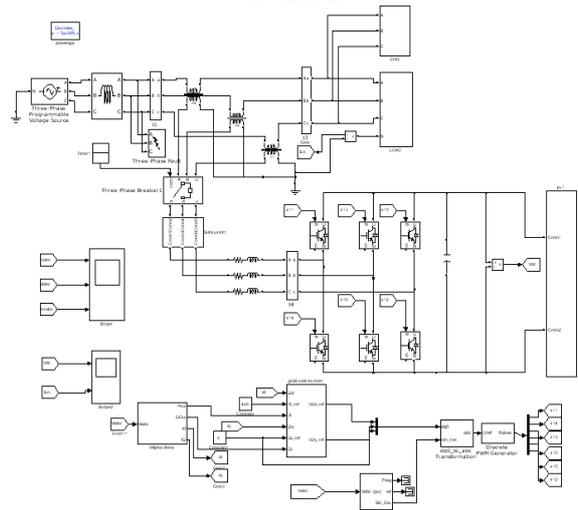
IV. MODELLING OF DFIG

DFIG is basically a WRIG which has a stator and rotor circuit. Stator circuit of the DFIG involve stator edge, secured stator focus having stator openings embedded in it and a balanced 3 arrange windings set 120 degree electrically isolated from one another. The contorting appropriated in nature, are ensured and are housed in the stator openings. Stator windings may be related in delta or star way.

DFIG CONTROL SCHEMES

Control of DFIG can be achieved by controlling the rotor side power flow. Hence controlling the power converter of rotor side (i.e RSC and GSC) we can control the DFIG completely. To achieve a smooth operation generally vector control method is adopted on both sides. In vector control scheme first the 3 phase quantities are transformed into 2 mutually orthogonal frames of reference and then by controlling this 2 components independently, decoupled control is achieved.

V. MATLAB SIMULATION & RESULTS USING DVR IN LINE



SIMULATIONS AND RESULTS

The system runs at 50 Hz frequency and total simulation time is chosen to be 0.35 seconds in each case. The scope connected to the V-I measurements at supply side as shown in fig 7.5.1

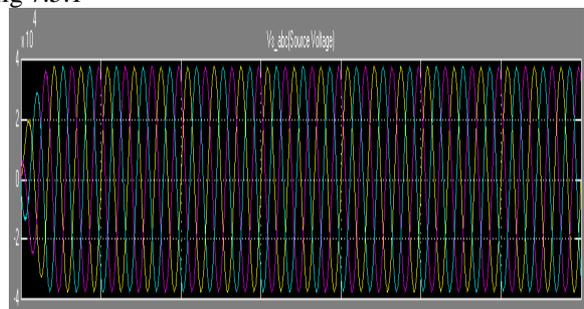


Fig6.5 Voltage Sag due to DFIG/disturbances

load side gives the simulations of supply voltage having sag and the voltage across load. We have taken DFIG and three phase programmable sources. The disadvantage of DFIG as a source is that it creates voltage misbalance in the system due

to which sag is produced. In Fig 7.5.2 it is observed that initially there is no voltage injection and power flow from DVR to the system. As no voltage sag is sensed. As soon as the load becomes unbalanced the voltage sag occurs.

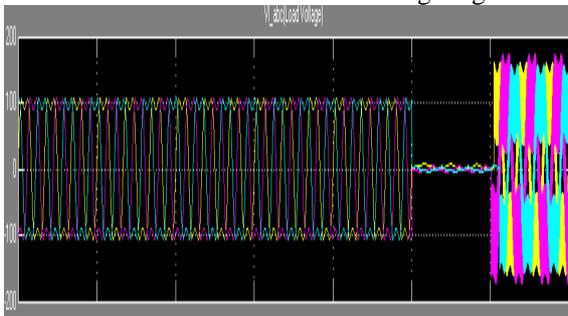


Fig6.6.Voltage Sag due to DFIG/disturbances

After the occurrence of Voltage Sag DVR comes into action and injects voltage which somewhat lessen the sag. Thus the system becomes more stable, as shown in fig 7.5.3 and 7.5.4. The sensing of DFIG disturbances by the DVR results in a rectified output voltage profile in which the voltage sag is compensated. The proposed methodology proves very unique that rectified the critical load changes disturbance problem.

The primary task of DVR is providing the high quality voltage to the critical loads. DVR enable the proposed system for providing a good power and voltage quality to the critical load. The controller output signals stabilize when all the phase voltages of the load attain the desired value. DVR gives high performance in injecting the more in-phase voltage with proper polarity and phase angle.

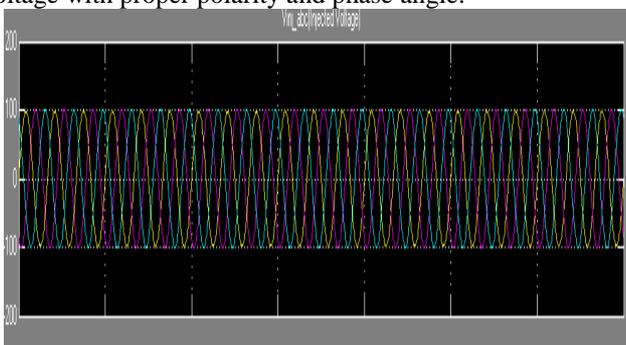


Fig 6.7. Injected Voltage due to DVR.

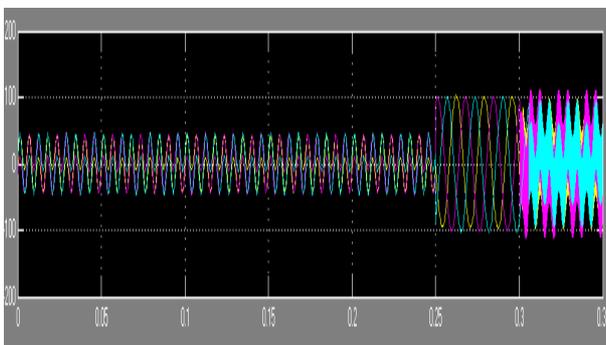


Fig6.8.Final rectified voltage by DVR

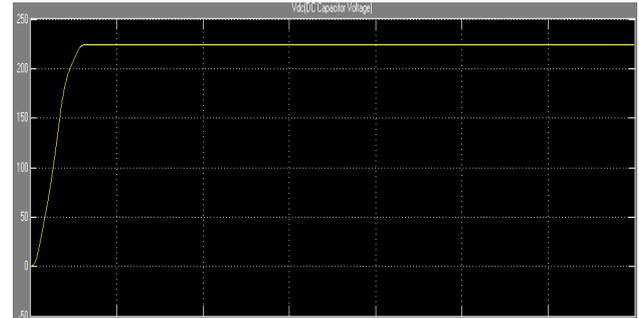
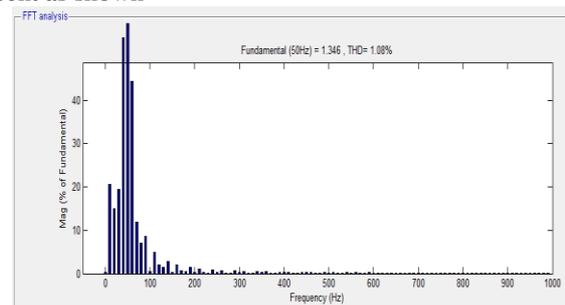


Fig6.9. Capacitor voltage output

The Fig 6.5 Illustrates how quickly the DVR responds for sudden changes to keep the sensitive load voltages at reference value. The calculated injection voltages exactly compensate the sag because the controller exactly calculates the missing voltage. Also the exponential rising curve of energy storage device above depicts that the sensing done by DVR for voltage sag and response given by capacitor storage resolves the problem, the single-phase PWM inverters managed by the control system generate the three distinct series inverter output voltages to compensate the source voltages at different sag level. In the last figure the total harmonic distortion are under control below 4 percent as shown



VI. CONCLUSIONS

In this work, cost effective & reliable custom power concept, dynamic voltage restorer is used to mitigate the voltage sags in the distribution system, thereby improving the performance of the system. The various control strategies are employed & tested for 11kV distribution system. The PI controller based DVR, fuzzy controller based DVR and PI-fuzzy controller based DVRs are connected step by step in the compensated feeder to compare their performances. The effectiveness of different control techniques based DVRs for static linear

FUTURE SCOPE OF WORK

With the increase use in number of sophisticated electronic devices by the industrial customers to increase their efficiency & productivity, it is important to ensure reliable power supply even under the system disturbances. To meet the varying system parameters & conditions, various other non-linear controllers can be used.

REFERENCES

- [1] C. Sankaran "Power Quality", CRC Press 2002.
- [2] N.G. Hingorani and L Gyugyi, "Understanding

- FACTS – Concepts and Technology of Flexible AC Transmission Systems”, Wiley, 2000.
- [3] S.Gupt, A.Dixit, N.Mishra, S.P.Singh, “Custom Power Devices for Power Quality Improvement: A Review”, International Journal of Research in Engineering & Applied Sciences, vol.2, February 2012.
- [4] S.Sundeeep and Dr. G. MadhusudhanaRao, “Modelling and Analysis of Custom Power Devices for Improve Power Quality”, International Journal of Electrical and Computer Engineering, vol.1, pp.43-48, September 2011.
- [5] M.H.J. Bollen, “What is Power Quality”, Electric, vol.66, pp.5-14, July 2003.
- [6] K.C. Divya and J.Ostergaard, “Battery Energy Storage Technology for Power Systems: An Overview”, Electric Power Systems Research, vol.79, pp.511-520, April 2009.
- [7] Rosli Omar, N.A.Rahim, Marizan Sulaiman, “Dynamic Voltage Restorer Application for Power Quality Improvement in Electrical Distribution System: An Overview”, Australian Journal of Basic and Applied Sciences, vol.5, pp.379-396, December 2011.
- [8] R. H.Salimin and M. S. A.Rahim, “Simulation Analysis of DVR Performance for Voltage Sag Mitigation”, IEEE 5th International Conference in Power Engineering and Optimization, pp.261-266, June 2011.
- [9] Jurado, Francisco, Valverde, M, “Fuzzy Logic Control of a Dynamic Voltage Restorer”, IEEE International Symposium on Industrial Electronics”, vol. 2, pp.1047-1052, May 2004.
- [10] A.Teke, K.Bayindir, M.Tumay, “A Robust Control of Dynamic Voltage Restorer Using Fuzzy Logic”, International Aegean Conference on Electrical Machines and Power Electronics, pp.55-60, September 2007.
- [11] A.Luo, C.Tang, Z.Shuai, J.Tang, X.Y. Xu, D.Chen, “Fuzzy-PI Based Direct-Output- Voltage Control Strategy for the Statcom Used in Utility Distribution systems”, IEEE Transactions on Industrial Electronics, vol.56, pp.2401-2411, July 2009.
- [12] S.Chauhan, V. Chopra, S.Singh, “Power System Transient Stability Improvement Using Fuzzy-PI Based STATCOM Controller”, 2nd International Conference on Power, Control and Embedded Systems, pp.1-6, December 2012.
- [13] N.H. Woodley, L. Morgan, A. Sundaram, “Experience with an Inverter-Based Dynamic Voltage Restorer”, IEEE Transactions on Power Delivery, vol.14, pp.1181-1186, July 1999.
- [14] H.P.Tiwari and S.K.Gupta, “Dynamic Voltage Restorer Against Voltage Sag”, International Journal of Innovation, Management and Technology, vol.1, pp.232-237, August 2010.
- [15] P.C Loh, D.M Vilathgamuwa, S.K Tang, H.L Long, “Multilevel Dynamic Voltage Restorer”, International Conference on Power System Technology, vol.2, pp.1673-1678, November 2004.
- [16] Ogunboyo Patrick Taiwo ; Remy Tiako ; Innocent E. Davidson “Voltage profile enhancement in low voltage 11/0.4 kV electric power distribution network using dynamic voltage restorer under three phase balance load” IEEE Pg. 991-996, September 2017.
- [17] Ragini Saxena; Manorma Kushwah,” Optimization of voltage sag/swell using dynamic voltage restorer (DVR)” 2016 International Conference on Electrical, Electronics, and Optimization Techniques (ICEEOT)Pages: 405 – 409