DESIGN & SIMULATION OF UPS INVERTER WITH PI CONTROLLER AND ANN

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ABSTRACT: Uninterruptible power supplies (UPSes) are used to provide backup power to critical loads, where even a temporary power failure causes large economic losses. The UPSes are used in applications like computer installations, process control in chemical plants, and general communication systems. The function of UPS is to provide a high quality sinusoidal voltage and for this it needs to be controlled. Conventional controllers cannot provide the exact sinusoidal output with the nonlinear loads. The objective of this project is to control inverter output voltage by maintaining it as a high quality sinusoidal shaping in nature with low total harmonic distortion (THD) for UPS applications. This is to be accomplished by an artificial neural network Controller in closed loop further the performance of the UPS inverter with neural network controller is to be compared with PI controller

Key word: UPSes, Power failure, THD, UPS inverter, Neural Network, PI Controller, MATLAB

I. INTRODUCTION

The uninterruptible power supplies are emergency power sources for applications where a power failure for a small amount of time causes large disturbances. The neural network control of UPS inverters is capable of producing low total harmonic distortion. Since a PI controller is designed from linear system theory, the output voltage of UPS inverter with PI controller will get distortion for nonlinear loads. The neural network had an advantage of adaptability to all types of loads, reduces the total harmonic distortion of output voltage, and hence used for the control of output voltage in UPS inverter applications. The design, implementation and testing of closed loop control of PI based UPS inverter and an artificial neural network based UPS inverter has been carried out. With the help of Matlab/Simulink toolbox. An artificial neural network is trained with the samples for different load values obtained from a controller, with back propagation algorithm. The simulation results of closed loop control of PI based UPS inverter and an artificial neural network based UPS inverter had been studied.

1.1 OBJECTIVE: The objective of this project is to control inverter output voltage by maintaining it as a high quality sinusoidal shaping in nature with low total harmonic distortion (THD) for UPS applications. This is to be accomplished by an artificial neural network Controller in closed loop further the performance of the UPS inverter with neural network controller is to be compared with PI controller.

II. LITERATURE SURVEY

Fuzzy logic and neural network technologies are in the process of fast evolution. The frontier of power electronics and motion control, which is already so complex and interdisciplinary, will be definitely extended far and wide by the AI techniques, and will provide a great challenge to the community of power electronic engineers.[10] A Method of Multiple Pulse Modulation with Selective Harmonic Elimination [13] can be effectively used to tackle the problem of harmonic elimination. However, it has limitations due to non-linearity involved in the voltage equations. The conventional methods of computations like Newton-Raphson cannot be successfully used as it requires the graphical approximation to be carried out as the initial stage. Thus, it is seen that the sinusoidal PWM technique is preferred over this method for controlling the inverters. To tackle the problem of non-linearity, intelligent techniques like Neural Networks and Evolutionary Programming are to be used [14-17]. Present work deals with the reduction of harmonics in the output voltage of a single phase inverter which forms a basic part in most of the thyristorised applications. Inverters are mainly used to control the ac drives [18]. The ANN controlled inverter output is used to control a 200W, single phase Induction motor in MATLAB simulation. The results are compared with Sinusoidal PWM controlled voltage driving the same motor load.

2.1 LITERATURE REVIEWS

[1] According to P.D.Ziogas, Uninterruptible power supplies are emergency power sources, which have wide spread applications in critical equipments, such as computers, automated process controllers, and in hospital instruments. With the rapid growth in high-efficiency power converters, more and more electrical loads are non-linear and generate harmonics. It is big challenge for a UPS to maintain a high quality sinusoidal output voltage under non-linear loading condition. The high quality sinusoidal output voltage is usually achieved by employing a combination of pulse width modulation (PWM) scheme and a second order filter at the output of the inverter. A multiple feedback-loop control scheme can be utilized to achieve good dynamic response and low total harmonic distortion. The control scheme is based on sensing the current in the capacitor of the output filter, using it in an inner feedback loop.

[2] According J.E.Quaicoe, Uninterruptible power supply (UPS) systems are used to provide continuous and reliable electrical power to loads in times of unexpected power failure. By connecting the UPS inverters in parallel, its

capacity is expandable. Parallel operation of inverters is gaining importance, because it increases system efficiency, provides redundancy and modularity.

[3] According W.E.Brumsickle, In a typical UPS inverter there is a Pulse-Width Modulation (PWM) converter and a second-order LC output filter. The UPS Inverter voltage can be controlled by using PWM techniques. The Sinusoidal PWM technique is used in this work, in which the width of each pulse is varied in proportion to the amplitude of the sine wave, which results in sinusoidal output voltage with a low THD. In this paper we propose neural and neuro-fuzzy based controllers with offline training for UPS inverter applications. Simulation results show that the developed neural and neuro fuzzy controllers can achieve low THD under nonlinear loading condition and good dynamic response under transient loading condition when compared to without controller and with PI controller.

III. CONVENTIONAL TOPOLOGIES OF UNITERRUPTIBLE POWER SUPPLIES

The industrial and commercial power committee of the IEEE defines an UPS as a system which is designed to provide power during all periods where in the normal or prime source of power is outside acceptable limits, without causing disruption of the flow of acceptable power to load. The historic function of the UPS was to provide backup power to critical loads to maintain the operation when the prime source of power fails. This chapter details the basic topologies of uninterruptible power supplies (UPSes), main parts of UPS system. It also explains the types of PWM inverters for UPS inverter applications.

3.1 MAIN PARTS OF UPSES: The block diagram of UPS is as shown in figure 3.1. There are two types UPS systems are available known as Static and Rotary. A solid state (static) UPS comprises of following units.

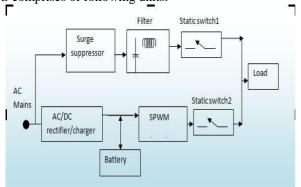


Fig 3.1: Block Diagram of General UPS System

- Conversion and inversion circuitry: All the UPS systems include a rectifier or charger circuit to provide DC to charge the battery and an inverter circuit that converts the DC power stored in battery to AC power to operate the loads. The exact type, nature, size and quality of this circuitry depend on the type of UPS.
- Battery: Other than the conversion and inversion circuit, another one is battery, which holds the energy that is used by UPS to run the load. The size of the battery dictates the size of the UPS and it is

- also proportional to the amount of energy that stored in UPS and run time of UPS for a given load. All batteries are rated in terms of their nominal voltage (in Volts), and their capacity (in Amp-hours or "Ah").
- Status indicators: Most of the UPS systems come
 with variety of indicators to indicate the current
 status of the UPS. Visual indicators (usually LEDs)
 are used to indicate general status as well as
 problem conditions. Audible indicators, some times
 called as alarms, are used to indicate some problem
 conditions like power failure.

Some of the typical status indicators are online, on battery, overload, Low battery, Load status, Battery status.

3.2 TYPES OF PWM INVERTER: The pulse width modulated (PWM) inverters are generally used for UPSes to maintain the output voltage as constant and regulates the output as sinusoidal shape in nature. The PWM type inverters are having an advantages of exclusion of lower order harmonics in the output voltage along with the control capabilities of output voltage, the harmonics in the inverter output are being eliminated by PWM technique, the filter circuit requirements are minimized. PWM techniques are characterized by constant amplitude pulses. The width of these pulses is modulated to control the inverter output voltage and for the reduction of harmonic content.

The Different PWM techniques are

- Single pulse modulation
- Multiple pulse modulation
- Sinusoidal pulse modulation.

The output voltage of single pulse width modulated inverter is of only one pulse located symmetrically. The width of the pulse is varied to control the inverter output voltage. The output voltage of single pulse width modulated inverter is quasi-square in nature and the harmonic content is more, which is not suitable for UPS applications.

IV. CLOSED LOOP CONTROL OF UPS INVERTER WITH CONVENTIONAL CONTROLLER

The core circuit of the UPS system is the inverter circuit, which is required for power conversion from dc (battery) to ac power. Inverter circuits can be broadly classified into two types,

- Voltage source inverters(VSI) and
- Current source inverters (CSI).

A voltage source inverter is one in which the dc source has small or negligible impedance, in other words a voltage source inverter has stiff dc voltage source at its input terminals and constant output voltage at its output. Current source inverter is fed with adjustable current from a dc source of high impedance, i.e. from a stiff dc current source. In a CSI fed with stiff current source, output current wave forms are not affected by load. In voltage source inverters, the input voltage is maintained constant and the output voltage does not depend on the load. However, the waveform of load current as well as magnitude depends upon the nature of the load impedance. In CSI the magnitude of output voltage and its waveform is dependent upon the nature of

load impedance.

4.1 BLOCK DIAGRAM OF UPS INVERTER WITH PI CONTROLLER

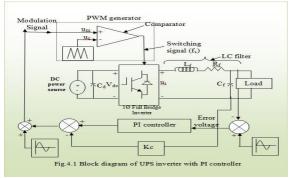


Fig 4.1: Block Diagram of UPS

The block diagram of UPS inverter with PI controller is shown in figure 4.1 UPS inverter typically consists of a DC power source, a full bridge PWM inverter and an LC filter. The full bridge inverter chops the DC input to a series of PWM pulses according to the modulating signal um. The function of the second order filter is to remove the higher frequency components of the chopped voltage ui, Rf represents the resistance of the filter inductance.

4.2 CLOSED LOOP CONTROL OF UPS INVERTER WITH PI CONTROLLER

Linear modeling of UPS inverter is used to obtain the transfer function, so that the PI controller parameter values can be easily tuned for a give step input to yield better stability conditions. Because the switching frequency (20 kHz) is usually several orders higher than the fundamental frequency of the AC output, the dynamics of the PWM inverter can be ignored such that the PWM inverter is modeled as a proportional block with a gain K equal to Vdc/Vc.Vdc is the voltage of DC source and Vc is the peak voltage of triangular carrier waveform.

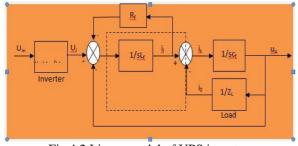


Fig.4.2 Linear model of UPS inverter

The design and implementation of closed loop control of PI based UPS inverter is described. PI controller was implemented in outer voltage loop to reduce the steady state error. Because the PI controller is designed from a linear system theory, the closed loop control of inverter for nonlinear loads will have the distortions in output voltage.

4.3 NEURAL NETWORK CONTROL OF UPS INVERTER

The neural network is an interconnection of artificial neurons. Artificial neural network is a nonlinear network, and has better robustness than linear system, improves the system performance for both the linear and nonlinear type of loads.

The many capabilities of neural networks, such as nonlinear functional approximation, learning, generalization, etc are in fact performed due to the nonlinear activation functions of each neuron.

4.4 ARTIFICIAL NEURON

Neural networks are simplified models of the biological nervous systems and therefore have drawn motivation from the kind of computing performed by a brain. An artificial neural network is highly interconnected network of larger number of processing elements called neurons. The basic neuron structure is as shown in figure 4.1

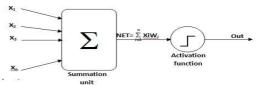


Fig.4.3 Simple model of artificial neuron

Three basic elements of the neuron model are:

- 1) A set of connecting links which fetch the input signals to the summation unit. Each of this connecting links is characterized by a weight or strength of its own. The weight of neuron may lie in the range that includes negative as well as positive values.
- 2) A summation unit or adder, for summing the input signals, weighted by the respective strength of neuron.
- 3) The last is an activation function for limiting the amplitude of the output of a neuron. The activation function is also referred as squashing function.

As shown in the figure 4.3, it consists of

an input vector, is given by [Xi],

 $[X_i]=[x_1,x_2,x_3,\ldots,x_n]$, the number inputs applied to a neuron, the Weights associated with these inputs are represented by a weight vector $[W_i]$.

Weight vector, is given by

$$[W_i] = [w_1, w_2, w_3, \dots, w_n].$$

The summation block, correspondingly the biological cell body, adds all the weighted inputs algebraically producing an output called as NET. This may be compactly stated in vector notation as follows

$$NET = \sum_{i=1}^{n} XiW_{i}$$

V. ARTIFICIAL NEURAL NETWORK

A neural network is a massively distributed processor made up of single processing units, which has a natural propensity for storing experimental knowledge and making it available for use. An artificial neural network is an information processing system that has certain performance characteristics, developed as generalizations of mathematical models of human cognition or neural biology, based on assumption that, Information processing occurs at many simple elements called neurons. Signals are passed between neurons over connection links. Each connection link has an associated weight, which in a typical neural net multiplies the signal transmitted. Each neuron applies an activation function to its net inputs to determine output.

5.1 Activation Functions

The activation function is used to calculate the output response of a neuron. For the neurons in the same layer, same activation functions are employed. There may be a linear as well as nonlinear activation functions. In this control of UPS inverter two types of activation functions are used.

i) Identity function (Purelin)

The function is given by,

F(x)=x; for all x.this is shown in the figure 4.2

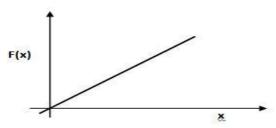


Fig. 5.1 Identity function

ii) Sigmoid function (Logsig)

These functions are usually S-shaped curves. The hyperbolic and logistic functions are commonly used. They are used in multilayer nets like back propagation network etc.

The logistic function, a sigmoid function with range from 0 to 1, is often used as the activation function for neural nets in which the desired values are in the range of 0 to 1. The function is given by,

$$F(x) = 1/1 + \exp(-\sigma x)$$

Where σ is steepness parameter, shown in figure 4.3

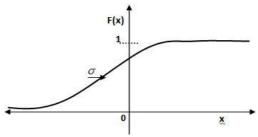


Fig.5.2: Sigmoid function

5.2 Feed Forward Back Propagation

These are the networks wherein for every input vector laid on the network, an output vector is calculated and this can be read from the output neurons. There is no feed back. Hence only a forward flow of information is present. Networks having this structure are called as feed forward networks.

Back propagation is a systematic method for training multilayer artificial neural networks. It's a multi layer forward network using extend gradient descent based delta – learning rule, commonly known as back propagation, it is trained by supervised learning method.

Training of a network by back propagation involves three stages. The feed forward of the input training pattern, the back propagation of the associated error, and the adjustment of weights. For training, the hidden layers uses sigmoid activation and output layer uses Purelin activation functions.

Back propagation training algorithm

The algorithm is as follows

Step 0: Initialize weights (set to random values in between - 0.5 to 0.5)

Step 1: While stopping condition is a false, do step 2-9.

Step 2: For each training pair, do steps 3-8.

Feed forward:

Step 3: Each input unit receives input signal and broadcasts this signal to

all units in the layer.

Step 4: Each hidden unit sums its weights input signals,

$$Z_{in_j} = v_{0j} + \sum_{i=1}^{n} x_i v_{ij}$$

Applies its activation function to compute its output signal, $Z_i=f(Z_i in_i)$,

And sends this signals to all units in the layer of output units. Step 5: Each output sums its weighted input signals,

$$y_i n_k = w_{ok} + \sum_{j=1}^{p} z_j w_{jk}$$

And applies its activation function to compute its output signal,

 $Y_k = f(y_in_k)$.

Back propagation of errors:

Step 6: Each output unit receives a target pattern corresponding to the input

Training pattern, computes its error information term,

$$\delta_{k}=(t_{k}-y_{k}) f'(y_{in_{k}}),$$

$$\Delta w_{jk} = \alpha \delta_k z_j,$$

Calculates its biases correction term (used to update w_{0k} later),

$$\Delta w_{0k} = \alpha \delta_{k}$$

and sends δ_k to units in the layer below.

Step 7: E ach hidden unit sums its delta inputs,

$$\delta_{-}in_{j} = \sum_{k=1}^{m} \delta_{k} w_{jk}$$

multiplies by the derivative of its activation function to calculate

its error information term,

$$\delta_j = \delta_- i n_j f'(z_- i n_j)$$

calculates its weight correction term (used to update v_{ij} later),

$$\Delta v_{ij} = \alpha \delta_j x_i$$

and calculates its biases correction term (used to $\;$ update v_{0j} later),

$$\Delta v_{0j} = \alpha \delta_j$$

Update weights and biases:

Step 8 Each output unit updates its bias and weights:

$$w_{jk}(new) = w_{jk}(old) + \Delta w_{jk}$$

Each hidden unit updates its biases and weights:

Step 9: Test stopping condition.

An epoch is a cycle through the entire set of training vectors. Typically, many epochs are required for training a back propagation neural net.

The design, implementation and testing of closed loop control of neural network based UPS inverter is described. The training of neural network is carried out with the help of Matlab NNtoolbox. The closed loop control of neural

network based UPS Inverter enhanced the wave shape of inverter output voltage, and hence reduced the total harmonic distortion of output compared with the PI controller. The UPS are known as emergency power sources, provides back up power to critical loads where an even temporary failure of power causes a large disturbance, used in applications like computer installations, hospital and in process controllers. The closed loop control schemes are used to maintain the inverter output voltage as constant for all types of loads. PI based control scheme maintains the inverter output voltage as constant for linear types of loads only, when the loads are nonlinear, the performance degrades. Hence an artificial neural network controller is used to control the output of UPS inverter.

5.3 PI CONTROLLER BASED UPS INVERTER

The Matlab/Simulink implementation of UPS inverter system is shown in figure 5.1 and the UPS inverter. The open loop UPS inverter simulation results are obtained and the output voltage and output current wave forms are shown below. From the open loop results it is observed that the total harmonic distortion of the output voltage is 2.68%. The figure 5.3 shows the UPS inverter output voltage and current with load changes from no-load to full load.

The Matlab/Simulink implementation of closed loop control of UPS inverter with PI controller output shown in respective figure is $k_p\!\!=\!\!3.1$ and $K_i\!\!=\!\!2200.$ The output voltage and output current waveforms with PI controller is shown in figure 5.5. From the simulation results it is observed that total harmonic distortion of the output voltage is 1.98%. The figure 5.6 shows the PI controller based UPS inverter output voltage and current with load changes from no-load to full-load.

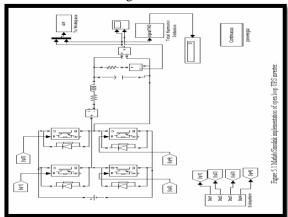


Fig 5.3: Matlab Simulink Implementation of Open Loop UPS

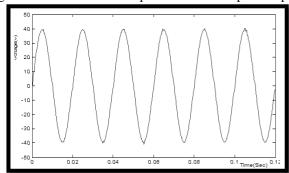


Fig.5.4 :Output voltage of inverter in open loop

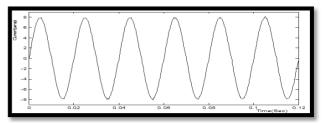


Figure 5.5:Output voltage of inverter in open loop

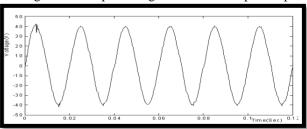


Fig.5.6: Inverter output voltage with the change in load from no-load to full load in open loop

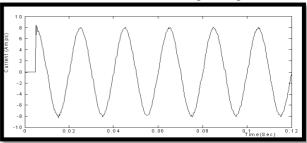


Fig.5.7: Inverter output current with the change in load from no-load to full-load in open loop

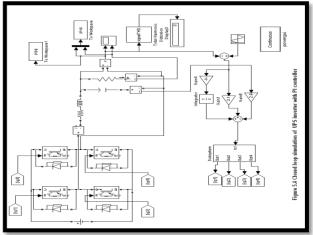


Fig: 5.8: Closed Loop UPS

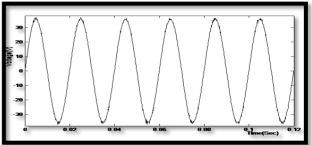


Fig 5.9: Inverter output voltage with PI controller

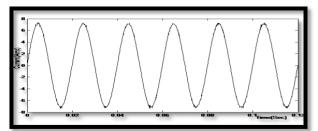


Fig 5.10: Inverter output current with PI controller

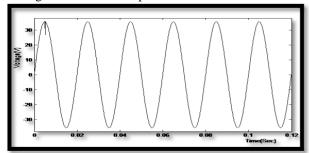


Fig.5.11: Inverter output voltage with change in load from no-load to full load with PI controller

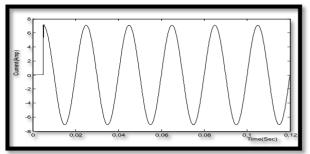


Fig. 5.12:Inverter output current with change in load from load to full load with PI controller.

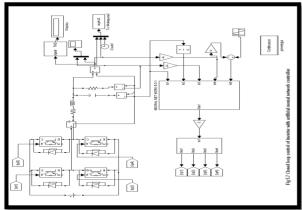


Fig 5.13: Proposed Design Of UPS

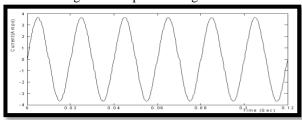


Fig. 5.14:Inverter output current with neural network controller

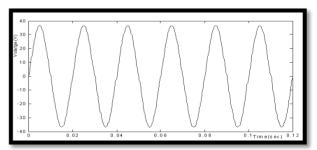


Fig.5.15: Inverter output voltage with neural network controller

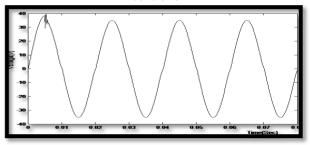


Fig. 5.16: Inverter output voltage with change in load from no-load to full load with neural network controller.

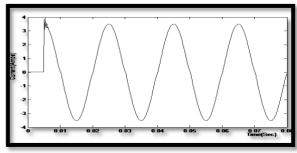


Fig 5.17: Inverter output current with change in load from no-load to full-load with neural network controller.

The design and implementation of UPS inverter system with conventional PI controller and artificial neural network controller are developed using Matlab/Simulink toolbox. The open loop and closed loop simulation results of the UPS inverter system with conventional PI controller and artificial neural network controller are obtained. From the simulation results it is observed that the artificial neural network controller based UPS inverter system gives better performance compared to the conventional PI controller based UPS inverter system. The THD for neural network controller is less compared to PI controller. Chapter 7 concludes the project work. The uninterruptible power supplies are emergency power sources for applications where a power failure for a small amount of time causes large disturbances. The neural network control of UPS inverters is capable of producing low total harmonic distortion. Since a PI controller is designed from linear system theory, the output voltage of UPS inverter with PI controller will get distortion for nonlinear loads. The neural network had an advantage of adaptability to all types of loads, reduces the total harmonic distortion of output voltage, and hence used for the control of output voltage in UPS inverter applications. The design, implementation and testing of closed loop control of PI based UPS inverter and an artificial neural

network based UPS inverter has been carried out. With the help of Matlab/Simulink toolbox. An artificial neural network is trained with the samples for different load values obtained from a controller, with back propagation algorithm. The simulation results of closed loop control of PI based UPS inverter and an artificial neural network based UPS inverter had been reported. This chapter concludes the project work.

VI. MAJOR CONTRIBUTIONS

The major contributions of the project work are listed below.

- ✓ The design and implementation of open loop control of UPS inverter has been developed by using Matlab/Simulink toolbox and the results are obtained and analyzed.
- ✓ The implementation of closed loop control scheme of UPS inverter with conventional PI controller has been developed and analyzed.
- ✓ The design, implementation and testing of closed loop control scheme of UPS inverter with an artificial neural network controller has been developed and the simulation results are obtained, analyzed and compared with conventional PI controller.

VII. CONCLUSION

The design and implementation of UPS inverter system with conventional PI controller and artificial neural network controller are developed using Matlab/Simulink toolbox. The open loop and closed loop simulation results of the UPS inverter system with conventional PI controller and artificial neural network controller are obtained, analyzed and compared. From the simulation results it is observed that the artificial neural network controller based UPS inverter system gives less THD in the output voltage and better performance compared to the PI controller.

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